

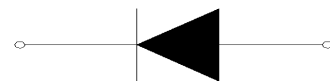
Standard Silicon Rectifiers Diode in R-6

Features

- Low reverse leakage
- High forward surge current capability
- Low reverse leakage current
- High surge current capability

Mechanical Data

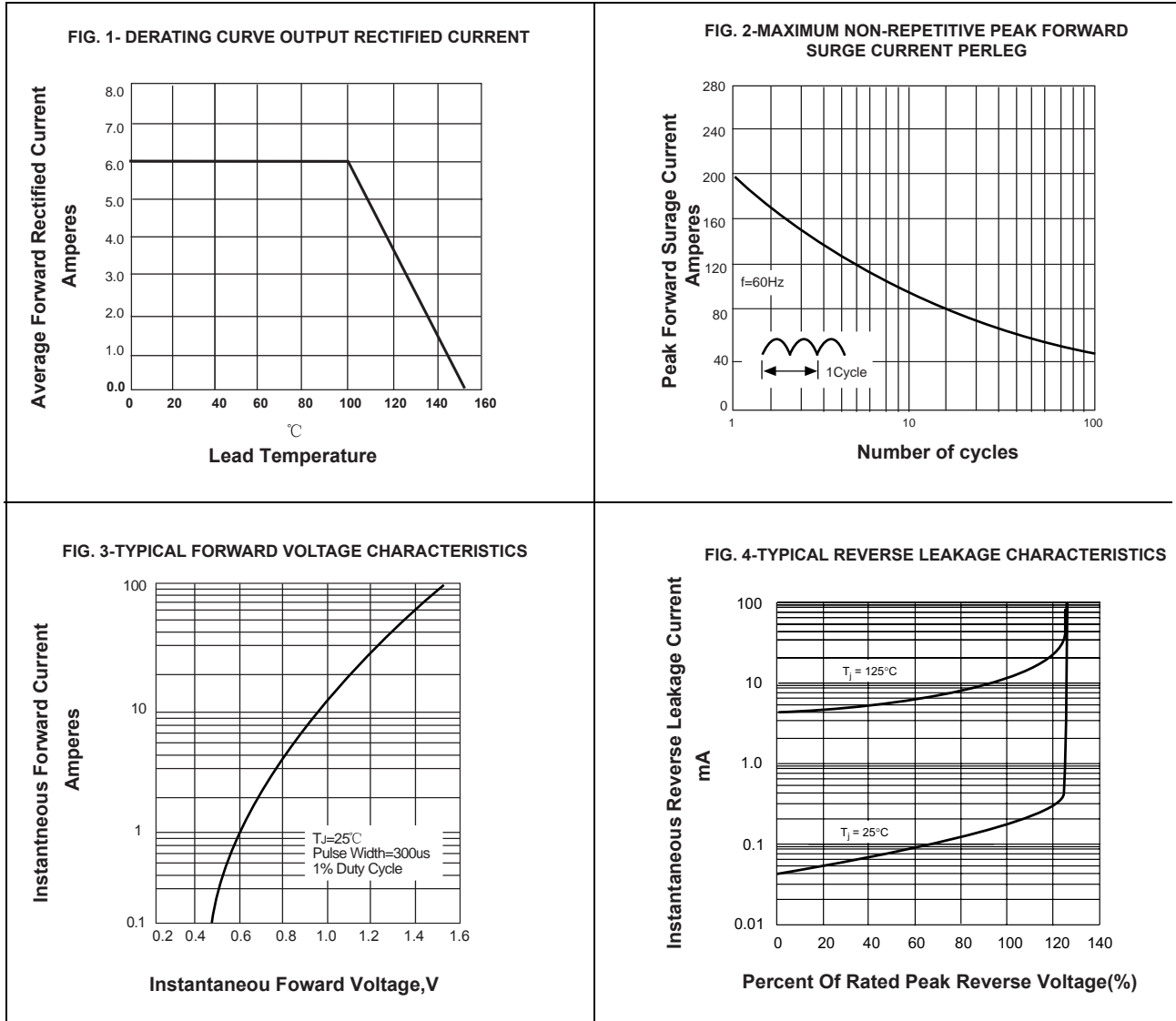
- **Case:** JEDEC R-6(P600)molded plastic
Lead free; RoHS compliant
- **Molding Compound Flammability Rating:**
UL 94 V-0
- **Terminals:** High temperature soldering guaranteed:
260 °C/10 sec. at terminals



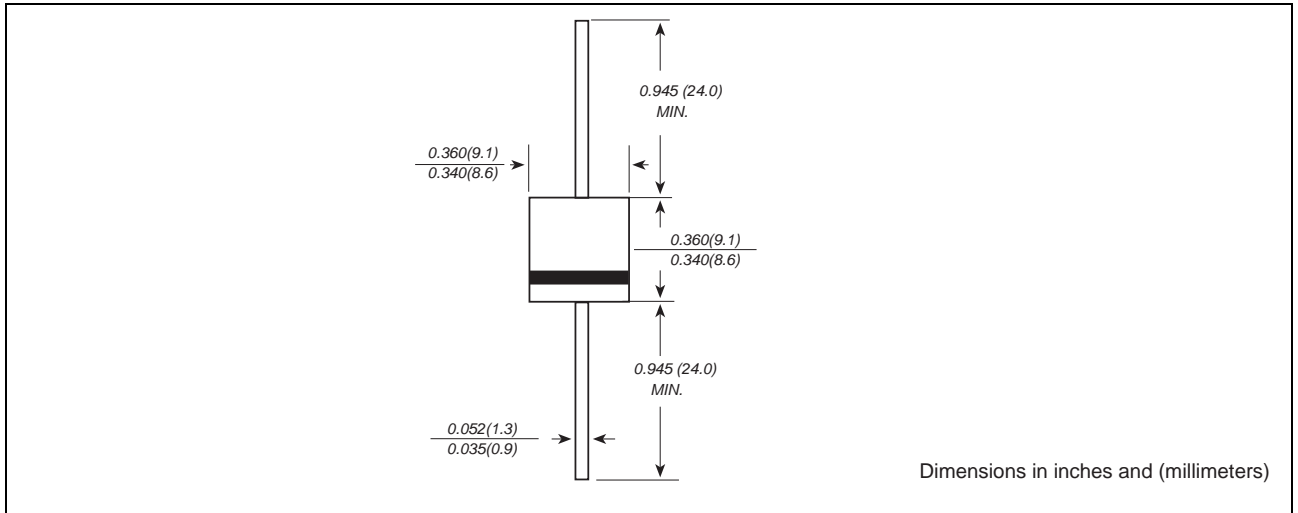
Maximum Ratings and Electrical Characteristics											
Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%											
Parameter	SYMBOLS	6A05	6A1	6A2	6A3	6A4	6A5	6A6	6A8	6A10	UNITS
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	500	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	350	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	500	600	800	1000	V
Maximum average forward rectified current at T _L =100°C	I _(AV)	6.0									A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	200.0									A
Maximum instantaneous forward voltage at 6.0A	V _F	1.10									V
Maximum DC reverse current T _A =25°C at rated DC blocking voltage T _A =125°C	I _R	10.0 500									uA
Typical junction capacitance (Note1)	C _J	100.0									pF
Typical thermal resistance	R _{qJA}	40.0									°C/W
Operating junction and storage temperature range	T _J ,T _{STG}	-55 to +150									°C

Note: 1.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

Typical Characteristics ($T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)



Package Dimensions



Ordering information

Order code	Package	Packaging option	Base quantity	Packaging specification
6A05 thur 6A10	R-6	Tape and BOX	500pcs / reel	EIA STD RS-481

Revision history

Date	Revision	Changes
23-May-2012	1.0	Initial release

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