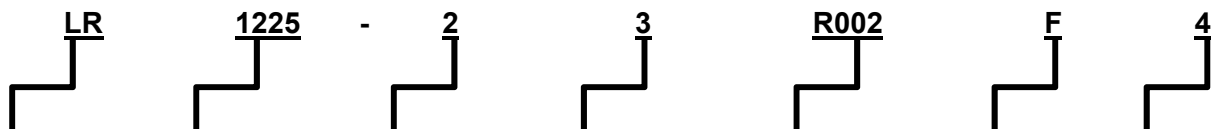


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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of RoHS for LR series wide terminal metal alloy low-resistance resistor.
- 1.2 Ideal for current detection under high current circuit.
- 1.3 The product is for general electronic purpose.

2 Explanation Of Part Numbers:



Type	Size (inch)	Number of Terminals	Rated Power	Resistance (4~6 Digits)	Tolerance	Packaging
Metal Alloy Low-Resistance Resistor	<ul style="list-style-type: none"> • 1020 • 1225 	2: 2 terminals	<ul style="list-style-type: none"> • 2=2.0W • 3=3.0W 	EX: R0005 = 0.5mΩ R002 = 2mΩ R003 = 3mΩ	D=± 0.5% F=± 1.0% J=± 5.0%	2=2,000pcs 4=4,000pcs

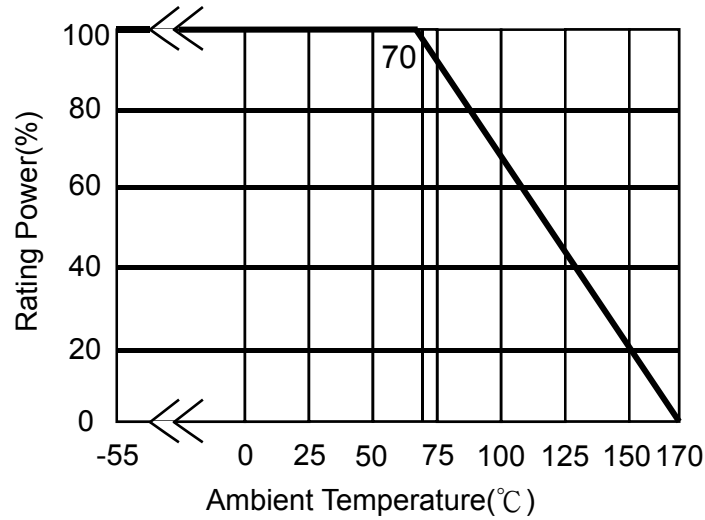
3 Product Specifications:

Type	# of Terminals	Rating Power	Rating Current	Overload Current	T.C.R. (ppm/°C)	Resistance Range (mΩ)	Operating Temperature Range
						D(±0.5%) F(±1%) J(±5%)	
LR1020	2	2W	$I_r = \sqrt{P/R}$	$I_o = \sqrt{5P/R}$	2mΩ~3mΩ : ≤±50	2~3	-55~170°C
LR1225	2	2W	I_r : Rating Current (A) P : Rating Power (W) R : R value(Ω)	I_o : Overload Current (A) P : Rating Power (W) R : R value(Ω)	2mΩ : ≤±50	2	
		3W			2mΩ : ≤±50	2	

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Written	Checked	Approved	Signing			

3.1 Power Derating Curve: Operating Temperature Range : - 55 ~+170 °C

For resistors operated in ambient temperatures 70°C, power rating shall be derated in accordance with the curve below:



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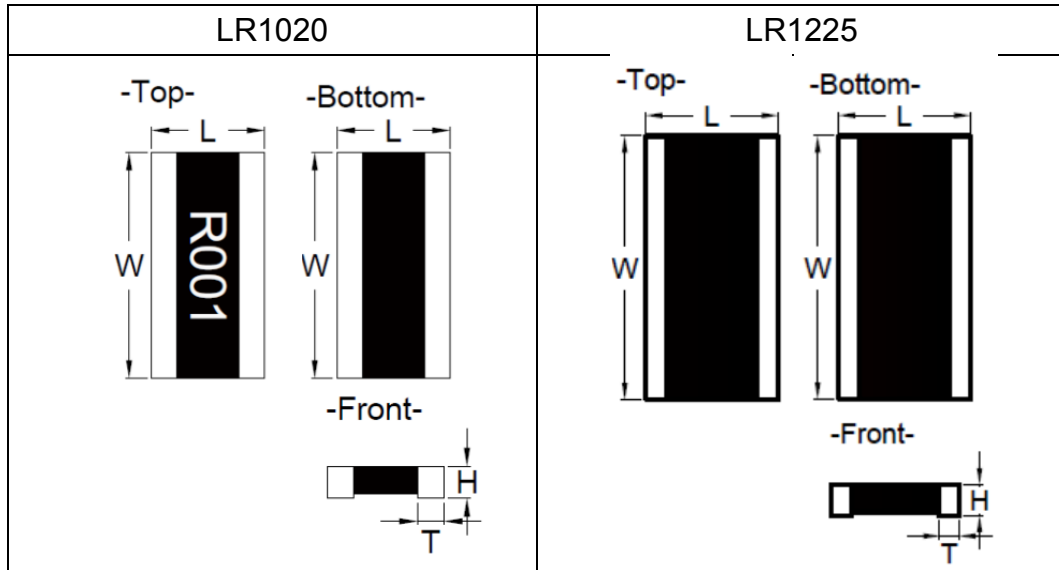
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4 Physical Dimensions:



Type	# of Terminals	Maximum Power Rating (Watts)	Resistance Range (mΩ)	Dimensions - in inches (millimeters)			
				L	W	T	H
LR1020	2	2	2	0.100±0.010 (2.54±0.254)	0.200±0.010 (5.08±0.254)	0.022±0.008 (0.558±0.203)	0.032±0.005 (0.82±0.127)
LR1225	2	2 & 3	2	0.126±0.010 (3.20±0.254)	0.250±0.010 (6.35±0.254)	0.020±0.010 (0.51±0.254)	0.040±0.010 (1.02±0.254)

4.1 Material of Alloy

Type	# of Terminals	Watts	Material	Resistance
LR1020	2	2.0	Iron-Chromium Aluminum Alloy	2mΩ~3 mΩ
LR1225	2	2.0	Iron-Chromium Aluminum Alloy	2mΩ
		3.0		

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5 Reliability Performance:

5.1 Electrical Performance:

Test Item	Conditions of Test	Test Limits											
Temperature Coefficient of Resistance (TCR)	<ul style="list-style-type: none"> TCR (ppm/°C) = $\frac{(R2-R1)}{R1 (T2-T1)} \times 10^6$ R1: resistance of room temperature R2: resistance of 150 °C T1: Room temperature T2: Temperature at 150 °C Refer to JIS C 5201-1 4.8 	Refer to Paragraph 3. general specifications											
Short Time Overload	Applied Overload for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Overload condition refer to below):	≤ ±0.5%											
	<table border="1"> <thead> <tr> <th>Type</th> <th># of Terminals</th> <th>Power (W)</th> <th># of rated power</th> </tr> </thead> <tbody> <tr> <td>LR1020</td> <td>2</td> <td>2.0</td> <td rowspan="2">5 times</td> </tr> <tr> <td rowspan="2">LR1225</td> <td rowspan="2">2</td> <td>2.0</td> </tr> <tr> <td>3.0</td> </tr> </tbody> </table>	Type	# of Terminals	Power (W)	# of rated power	LR1020	2	2.0	5 times	LR1225	2	2.0	3.0
Type	# of Terminals	Power (W)	# of rated power										
LR1020	2	2.0	5 times										
LR1225	2	2.0											
		3.0											
Refer to JIS C 5201-1 4.13													

5.2 Mechanical /Constructional Performance:

Test Item	Conditions of Test	Test Limits
Resistance to Solder Heat	The tested resistor be immersed 25 mm/sec into molten solder of 260±5°C for 10±1secs. Then the resistor is left in the room for 1 hour, and measured its resistance variance rate. Refer to JIS-C5201-1 4.18	≤ ±0.5%
		No evidence of mechanical damage
Solderability	Add flux into tested resistors, immersion into solder bath in temperature 245±5°C for 3±0.5secs. Refer to JIS-C5201-1 4.17	Solder coverage over 95%
Vibration	The resistor shall be mounted by its terminal leads to the supporting terminals on the solid table. The entire frequency range :from 10 Hz to 55 Hz and return to 10 Hz, shall be transferred in 1 min. Amplitude : 1.5mm This motion shall be applied for a period of 4 hours in each 3 mutually perpendicular directions (a total of 12hrs) Refer to JIS-C5201-1 4.22	≤ ±0.5%
		No evidence of mechanical damage

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5.3 Environmental Performance:

Test Item	Conditions of Test	Test Limits						
Low Temperature Exposure (Storage)	Put the tested resistor in chamber under temperature $-55\pm 2^{\circ}\text{C}$ for 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.23.4	$\leq \pm 0.5\%$ No evidence of mechanical damage						
High Temperature Exposure (Storage)	Put tested resistor in chamber under temperature $170\pm 5^{\circ}\text{C}$ for 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.23.2	$\leq \pm 0.5\%$ No evidence of mechanical damage						
Temperature Cycling (Rapid Temperature Change)	Put the tested resistor in the chamber under the temperature cycling which shown in the following table shall be repeated 1,000 times consecutively. Then leaving the tested resistor in the room temperature for 60 minutes, and measure its resistance variance rate. <table border="1" style="margin-left: 40px;"> <thead> <tr> <th colspan="2">Testing Condition</th> </tr> </thead> <tbody> <tr> <td>Lowest Temperature</td> <td>$-55 +0/-10^{\circ}\text{C}$</td> </tr> <tr> <td>Highest Temperature</td> <td>$150 +10/-0^{\circ}\text{C}$</td> </tr> </tbody> </table> Refer to JIS-C5201-1 4.19	Testing Condition		Lowest Temperature	$-55 +0/-10^{\circ}\text{C}$	Highest Temperature	$150 +10/-0^{\circ}\text{C}$	$\leq \pm 0.5\%$ No evidence of mechanical damage
Testing Condition								
Lowest Temperature	$-55 +0/-10^{\circ}\text{C}$							
Highest Temperature	$150 +10/-0^{\circ}\text{C}$							
Moisture Resistance (Climatic Sequence)	Put the tested resistor in chamber and subject to 10 cycles of damp heat and without power. Each one of which consists of the steps 1 to 7 (Figure 1). Then leaving the tested resistor in room temperature for 24 hr, and measure its resistance variance rate. Refer to MIL-STD 202 Method 106	$\leq \pm 0.5\%$ No evidence of mechanical damage						
Bias Humidity	Put the tested resistor in chamber under $85\pm 5^{\circ}\text{C}$ and $85\pm 5\% \text{RH}$ with 10% bias and load the rated current for 90 minutes on, 30 minutes off, total 1,000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.24	$\leq \pm 0.5\%$ No evidence of mechanical damage						

5.4 Operational Life Endurance:

Test Item	Conditions of Test	Test Limits
Load Life	Put the tested resistor in chamber under temperature $70\pm 2^{\circ}\text{C}$ and load the rated current for 90 minutes on 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.25	$\leq \pm 1.0\%$ No evidence of mechanical damage

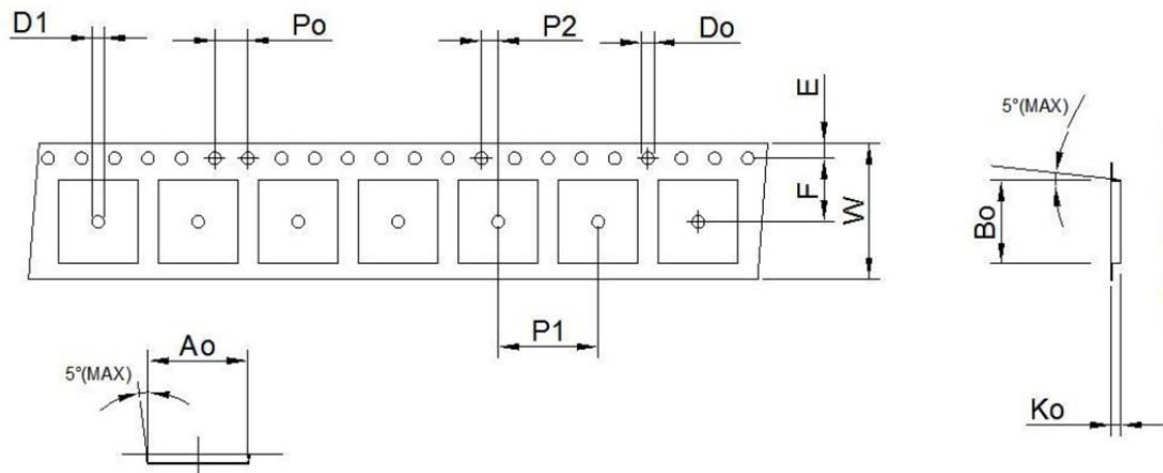
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6 Measurement Point :

Bottom electrode		Unit : mm	
Type	DIM	A	B
	LR1020		1.97±0.10
LR1225		2.7±0.10	3.2±0.10

Taping specifications:

6.1 Tape Dimensions:



Unit: mm

Type-Terminals	DIM										
	Ao	Bo	W	E	F	Ko	Po	P1	P2	Do	D1
LR1020	2.9±0.1	5.45±0.1	12.0±0.2	1.75±0.1	5.5±0.1	1.33±0.1	4.0±0.1	4.0±0.1	2.0±0.1	1.5±0.1	---
LR1225	3.5±0.1	6.75±0.1	12.0±0.1	1.75±0.1	5.5±0.1	1.3±0.1	4.0±0.1	4.0±0.1	2.0±0.1	1.5±0.1	---

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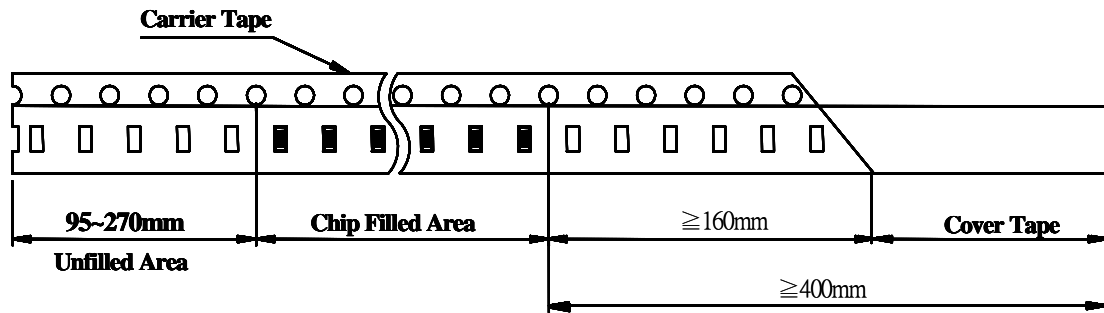
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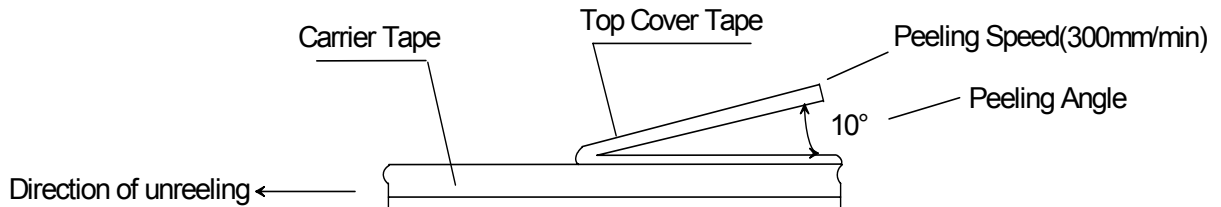
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6.2 Lead Dimensions:



6.3 Cover Tape Peel off Strength:

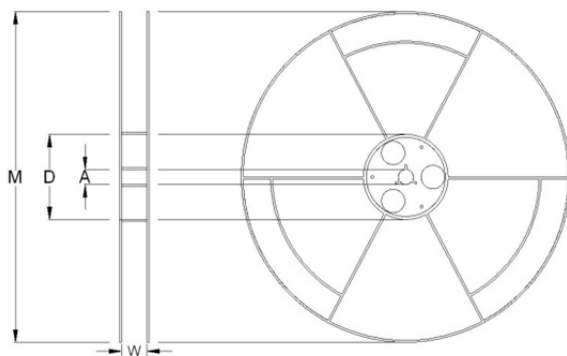
Specification value: 0.3~1.0N(30~100gf)



6.4 Packaging model:

Type	# of Terminals	Tape width	Max. Packaging Quantity (pcs/reel)
			Embossed Plastic Type
			4mm pitch
LR1020	2	12mm	2000pcs/4000pcs
LR1225	2	12mm	4000

6.5 Reel Dimensions:



Reel Type / Tape	W	M	A	D
7" reel for 12 mm tape	13.8 ± 0.5	178 ± 2.0	13.5 ± 0.5	80.0 ± 1.0

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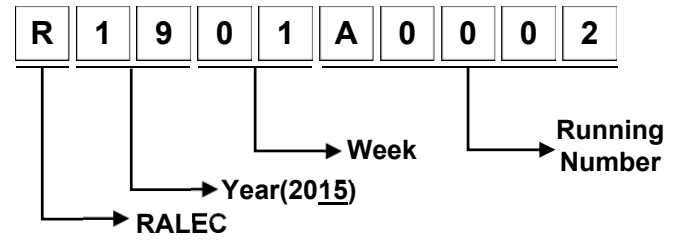
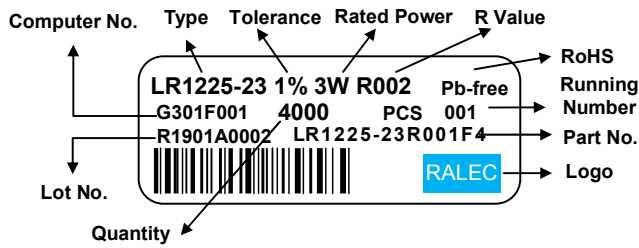
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6.6 Label:

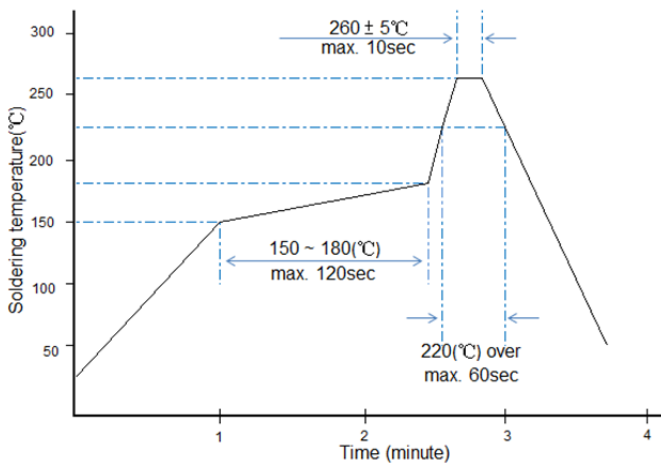


8 Technical application note (This is for recommendation, please customer perform adjustment according to actual application)

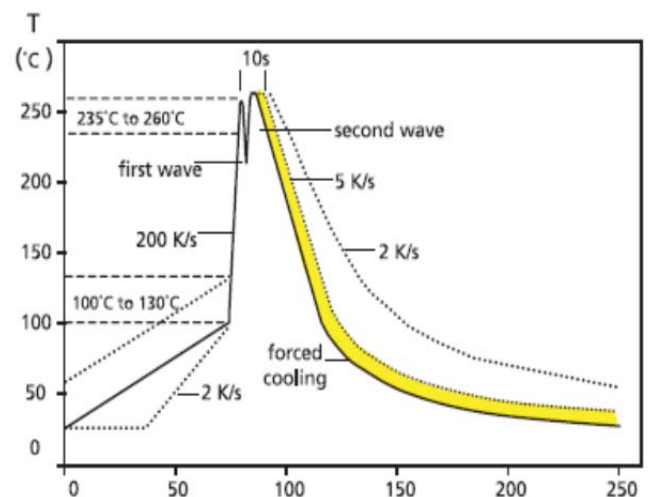
8.1 Recommend soldering method:

8.1.1 Typical examples of soldering processes that provides reliable joints without any damage are given in below:

8.1.2 Soldering Iron: temperature $350^{\circ}\text{C} \pm 10^{\circ}\text{C}$, dwell time shall be less than 3 sec.



Recommended IR Reflow Soldering Profile



Recommended double-wave Soldering Profile

Typical values (solid line)

Process limits (dotted line)

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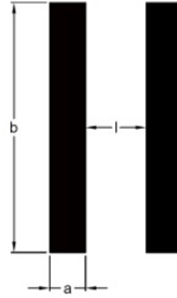
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8.2 Recommend Land Pattern:

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



Type	# of Terminals	Maximum Power Rating (Watts)	Resistance Range (mΩ)	Dimensions - in millimeters		
				a	b	i
LR1020	2	2	2.0~3.0	1.25	5.65	1.00
LR1225	2	2	2.0	1.00	7.00	1.70
		3				

8.3 The characteristic of Fe/Cr/Al alloy material:

Because of including magnetism, inductor will be generated under high frequency circuit then to cause value shift and influence customer application. If there is related application shall be noted especially or discuss with original factory.

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8.4 Environment Precautions:

This specification product is for general electronic use, RALEC will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with RALEC.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl₂、H₂S、NH₃、SO₂ and NO₂.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

8.5 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

8.6 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resistor will be overloaded. There might be machinery damage due to the climbing temperature.
- (d) If the resistor will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

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9 Storage and transportation requirement:

- 9.1 The temperature condition must be controlled at $25\pm 5^{\circ}\text{C}$, the R.H. must be controlled at $60\pm 15\%$. The stock can maintain quality level in two years.
- 9.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl_2 、 H_2S 、 NH_3 、 SO_2 and NO_2 .
- 9.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

10 Attachments

- 10.1 Document Revise Record (QA-QR-027)

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